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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10052428	FILING DATE 01/18/2002	CLASS 029	SUBCLASS 740	GAU 3729	EXAMINER Thinh
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****APPLICANTS:** Huber Robert;

****CONTINUING DATA VERIFIED:** *Thinh*
 THIS APPLICATION IS A DIV OF 09/716,019 11/17/2000
 WHICH CLAIMS BENEFIT OF 60/225,427 08/15/2000

**** FOREIGN APPLICATIONS VERIFIED:** *None*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		2002 P 00898 US	
Verified and Acknowledged Examiners's initials <i>[Signature]</i>			
TITLE : Defective circuit scanning device and method			

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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